L	Hits	Search Text	DB	Time stamp
Number				
1	100	LDMOS with CMOS	USPAT;	2003/10/14
			US-PGPUB	14:05
2	41	(LDMOS with CMOS) same power	USPAT;	2003/10/14
-		(22302	US-PGPUB	14:06

				<b>a</b> :
L Number	Hits	Search Text	DB	Time stamp
-	262	(LDMOS or (lateral adj DMOS) or (lateral adj diffus\$3 adj mos)) and ("V.sub.th." or (threshold nearl voltage))	USPAT; US-PGPUB	2003/07/28 17:51
-	93336	(HV with LV) or (low with high with voltage)	USPAT; US-PGPUB	2003/07/28
-	26320	((HV with LV) or (low with high with	USPAT; US-PGPUB	2003/07/28 17:49
-	12576	voltage)) same (transistor or MOS) (((HV with LV) or (low with high with voltage)) same (transistor or MOS)) and	USPAT; US-PGPUB	2003/07/28 17:49
-	10706	<pre>(threshold with voltage) ((((HV with LV) or (low with high with voltage)) same (transistor or MOS)) and (threshold with voltage)) not</pre>	USPAT; US-PGPUB	2003/07/28 17:50
-	93	<pre>(adjustment) (((((HV with LV) or (low with high with voltage)) same (transistor or MOS)) and (threshold with voltage)) not</pre>	USPAT; US-PGPUB	2003/07/28 17:50
-	4	<pre>(adjustment)) and ldmos (((((HV with LV) or (low with high with voltage)) same (transistor or MOS)) and (threshold with voltage)) not (adjustment)) and ldmos) not ((LDMOS or (lateral adj DMOS) or (lateral adj</pre>	USPAT; US-PGPUB	2003/07/28 17:50
	103	<pre>diffus\$3 adj mos)) and ("V.sub.th." or (threshold nearl voltage))) ((((HV with LV) or (low with high with</pre>	USPAT;	2003/07/28
	103	voltage)) same (transistor or MOS)) and (threshold with voltage)) and ldmos	US-PGPUB	17:50
-	6	(((((HV with LV) or (low with high with voltage)) same (transistor or MOS)) and (threshold with voltage)) and ldmos) not ((LDMOS or (lateral adj DMOS) or (lateral	USPAT; US-PGPUB	2003/07/28 17:50
_	765	adj diffus\$3 adj mos)) and ("V.sub.th." or (threshold nearl voltage))) (LDMOS or (lateral adj DMOS) or (lateral	USPAT;	2003/07/28
_	503	adj diffus\$3 adj mos)) ((LDMOS or (lateral adj DMOS) or (lateral adj diffus\$3 adj mos))) not ((LDMOS or	US-PGPUB USPAT; US-PGPUB	17:51 2003/07/28 18:14
		(lateral adj DMOS) or (lateral adj diffus\$3 adj mos)) and ("V.sub.th." or (threshold nearl voltage)))		
_	14		USPAT	2003/07/28
-	5	("4626879"   "4628341"   "4811075"   "4994889"   "4994904").PN.	USPAT	2003/07/28 17:55
-	0	RE37424.URPN.	USPAT	2003/07/28 18:05
-	9	("4120707"   "4628341"   "4710241"   "4887142"   "4890142"   "4918026"   "4928164"   "4962052"   "5156989").PN.	USPAT	2003/07/28 18:05
-	29	4120707.URPN.	USPAT	2003/07/28 18:06
_	0	6451640.URPN.	USPAT	2003/07/28 18:10
_	11	("4628341"   "4918026"   "5043788"   "5047358"   "5254487"   "5468666"   "5527722"   "5578855"   "5747850"	USPAT	2003/07/28 18:10
-	13	"5795803"   "5847432").PN. 5847432.URPN.	USPAT	2003/07/28
_	1	"4866002".PN.	USPAT	2003/07/28 18:13
-	14	5043788.URPN.	USPAT	2003/07/28 18:13
-	3	("4258379"   "4379726"   "4894692").PN.	USPAT	2003/07/28 18:13
-	5	("5726477"   "5852314"   "5917222"   "6258644"   "6277682").PN.	USPAT	2003/07/28
-	4939	high with low with (voltage or power) with (CMOS or MOSFET)	USPAT; US-PGPUB	2003/07/29

Search History 10/14/03 3:22:23 PM Page 3

				1
_	740	(high with low with (voltage or power)	USPAT;	2003/07/29
		with (CMOS or MOSFET)) and 438/\$.ccls.	US-PGPUB	13:46
_	7	("4435896"   "4535529"   "4651406"	USPAT	2003/07/29
		"4753898"   "5607868"   "5712201"		11:23
		"5834352").PN.		]
_	5	6043128.URPN.	USPAT	2003/07/29
				11:23
_	6	("4651406"   "5243210"   "5407849"	USPAT	2003/07/29
		"5966321"   "5989949"   "6043128").PN.		11:26
_	92651	LDMOS with (MISFET) or (EPROM or EEPROM	USPAT;	2003/07/29
		or DRAM)	US-PGPUB	16:02
_	24	(LDMOS with (EPROM or EEPROM or DRAM))	USPAT;	2003/07/29
			US-PGPUB	16:41
_	1	("5296393").PN.	USPAT;	2003/07/29
		· ·	US-PGPUB	17:27
_	3	(("20010005037") or ("6255207") or	USPAT;	2003/07/29
		("5814555")).PN.	US-PGPUB	17:35
_	1	("5621241").PN.	USPAT;	2003/07/30
ļ			US-PGPUB	11:25
_	5	LDMOS with (low adj voltage adj CMOS)	USPAT;	2003/08/10
,			US-PGPUB	11:12
_	0	LDMOS with (low adj voltage adj MOS)	USPAT;	2003/08/10
			US-PGPUB	11:13
l <b>-</b>	0	(LDMOS with (low adj voltage adj	USPAT;	2003/08/10
		transistor)) and CMOS	US-PGPUB	11:14
_	44	(LDMOS with (low adj voltage)) and CMOS	USPAT;	2003/08/10
		`	US-PGPUB	11:16
<u>-</u>	1	("5,242,841").PN.	USPAT;	2003/08/10
	_	, , , , .= ,	US-PGPUB	11:16